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** CONTINUING DATA ***** *TN* *None*
 ** FOREIGN APPLICATIONS ***** *TN*
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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after				
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Allowance <i>TN</i> Examiner's Signature Initials				

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TITLE

Semiconductor devices with enlarged recessed gate electrodes and methods of fabrication therefor

☐ All Fees